

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): **Manabe, Kazutaka**

Serial No.: N/A

Filed: **Herewith**

Title: **Semiconductor Device and Its  
Manufacturing Method**

Attorney Docket No.: **N09692401WD1**

Group Art Unit (of Parent Application): **2815**

Examiner: **N/A**

**PRELIMINARY AMENDMENT**

5 Mail Stop Patent Application  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

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A. Introductory Comments

The following is a Preliminary Amendment for the attached patent application.

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This response is presented in the revised amendment format set forth in 1265 Off. Gaz. Pat. Office 87 (Dec. 17, 2002) and so is subject to waiver of the provisions of 37 C.F.R. §1.21(a), (b) and (c).

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37 C.F.R. §1.10

I hereby certify that this is being deposited with the U.S. Postal Service "Express Mail Post Office to Addressee" service under 37 CFR § 1.10 on the date indicated below and is addressed to:

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Mail Stop Patent Application  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

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